

Amendments to the Specification:

Please replace the paragraph beginning on page 1, line 22, with the following amended paragraph:

Usually, prior art group delay equalizers have an all pass transfer function with pole-zero pairs which lie symmetrically with respect to the real axis of the complex frequency plane, with the poles and zeros of the pairs lying symmetrically with respect to the imaginary axis of that plane, whereby the poles are located in the left half of the plane and the zeros in the right half. ~~With~~In other words, with a group delay equalizer with two pole-zero pairs, the two poles are located at $p = -\sigma \pm j\omega_s$ and the two zeros are located at $p = \sigma \pm j\omega_s$ where ω_s represents the shift along the imaginary axis and σ the shift along the real axis.